IXYS

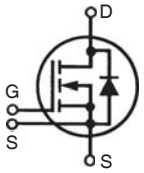
Advance Technical Information

X3-Class HiPerFET™ Power MOSFET

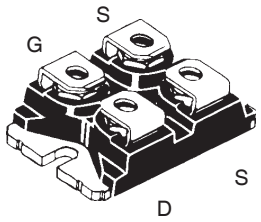
IXFN240N25X3

V_{DSS} = 250V
I_{D25} = 240A
$R_{DS(on)}$ ≤ 4.5mΩ

N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Diode



miniBLOC, SOT-227
E153432



G = Gate D = Drain
S = Source

Symbol	Test Conditions		Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C		250	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ		250	V
V_{GSS}	Continuous		± 20	V
V_{GSM}	Transient		± 30	V
I_{D25}	T_C = 25°C (Chip Capability)		240	A
$I_{L(RMS)}$	External Lead Current Limit		200	A
I_{DM}	T_C = 25°C, Pulse Width Limited by T_{JM}		400	A
I_A	T_C = 25°C		200	A
E_{AS}	T_C = 25°C		3	J
P_D	T_C = 25°C		695	W
dv/dt	$I_S \le I_{DM}$, $V_{DD} \le V_{DSS}$, $T_J \le 150°C$		20	V/ns
T_J			-55 ... +150	°C
T_{JM}			150	°C
T_{stg}			-55 ... +150	°C
V_{ISOL}	50/60 Hz, RMS	t = 1 minute	2500	V~
	$I_{ISOL} \le$ 1mA	t = 1 second	3000	V~
M_d	Mounting Torque		1.5/13	Nm/lb.in
	Terminal Connection Torque		1.3/11.5	Nm/lb.in
Weight			30	g

Symbol	Test Conditions (T_J = 25°C Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0V, I_D = 3mA	250			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 8mA	2.5		4.5	V
I_{GSS}	V_{GS} = ± 20V, V_{DS} = 0V			± 200	nA
I_{DSS}	$V_{DS} = V_{DSS}$, V_{GS} = 0V			25	μA
	T_J = 125°C			2.5	mA
$R_{DS(on)}$	V_{GS} = 10V, I_D = 120A, Note 1		3.5	4.5	mΩ

Features

- International Standard Package
- miniBLOC, with Aluminium Nitride Isolation
- Isolation Voltage 2500V~
- High Current Handling Capability
- Fast Intrinsic Diode
- Avalanche Rated
- Low $R_{DS(on)}$

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

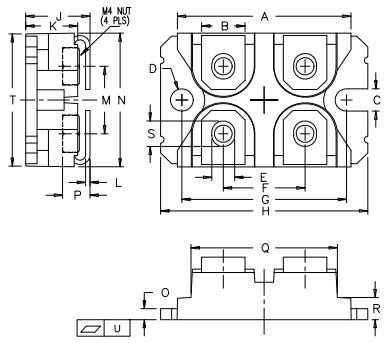
Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max	
g_{fs}	V_{DS} = 10V, I_D = 60A, Note 1	80	135		S
R_{Gi}	Gate Input Resistance		1.8		Ω
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		23.8		nF
C_{oss}			3.7		nF
C_{rss}			1.5		pF
	Effective Output Capacitance				
$C_{o(er)}$	Energy related: V_{GS} = 0V, V_{DS} = 0.8 • V_{DSS}		1400		pF
$C_{o(tr)}$	Time related: V_{GS} = 0V, V_{DS} = 0.8 • V_{DSS}		5480		pF
$t_{d(on)}$	**Resistive Switching Times**		36		ns
t_r	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 120A		32		ns
$t_{d(off)}$	R_G = 1Ω (External)		180		ns
t_f			14		ns
$Q_{g(on)}$	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 120A		345		nC
Q_{gs}			112		nC
Q_{gd}			72		nC
R_{thJC}				0.18	°C/W
R_{thCS}			0.05		°C/W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max	
I_S	V_{GS} = 0V			240	A
I_{SM}	Repetitive, pulse Width Limited by T_{JM}			960	A
V_{SD}	I_F = 100A, V_{GS} = 0V, Note 1			1.4	V
t_{rr}	I_F = 120A, -di/dt = 100A/μs		165		ns
Q_{RM}	V_R = 100V		3.7		μC
I_{RM}			45.6		A

Note 1. Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

SOT-227B (IXFN) Outline



(M4 screws (4x) supplied)

SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	1.240	1.255	31.50	31.88
B	.307	.323	7.80	8.20
C	.161	.169	4.09	4.29
D	.161	.169	4.09	4.29
E	.161	.169	4.09	4.29
F	.587	.595	14.91	15.11
G	1.186	1.193	30.12	30.30
H	1.496	1.505	38.00	38.23
J	.460	.481	11.68	12.22
K	.351	.378	8.92	9.60
L	.030	.033	0.76	0.84
M	.496	.506	12.60	12.85
N	.990	1.001	25.15	25.42
O	.078	.084	1.98	2.13
P	.195	.235	4.95	5.97
Q	1.045	1.059	26.54	26.90
R	.155	.174	3.94	4.42
S	.186	.191	4.72	4.85
T	.968	.987	24.59	25.07
U	-.002	.004	-0.05	0.1

ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

Fig. 1. Output Characteristics @ T_J = 25°C

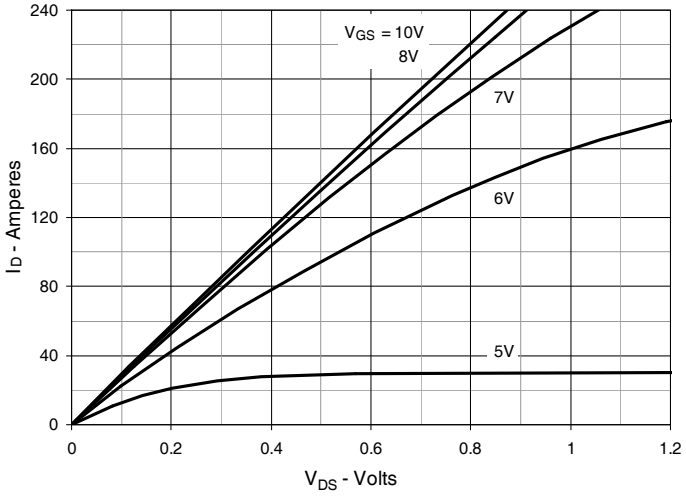


Fig. 2. Extended Output Characteristics @ T_J = 25°C

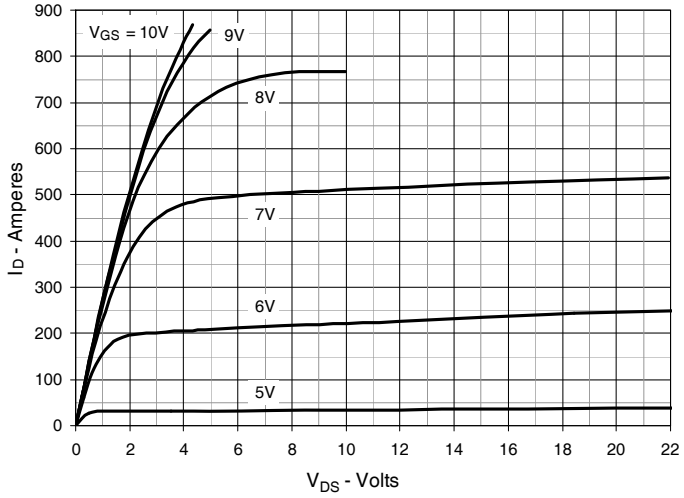


Fig. 3. Output Characteristics @ T_J = 125°C

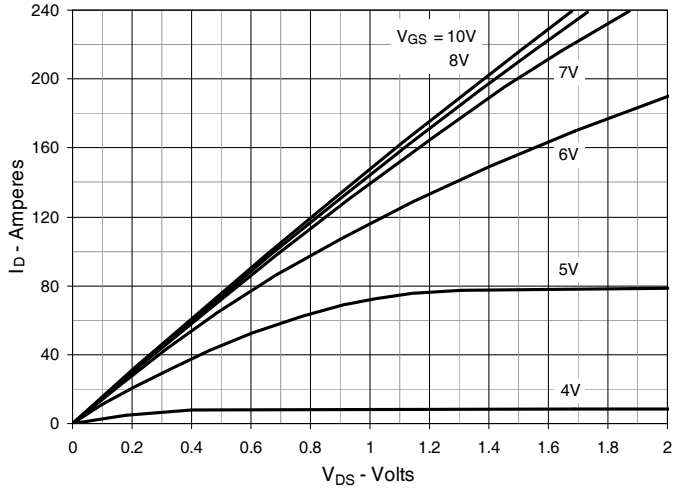


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 120A Value vs. Junction Temperature

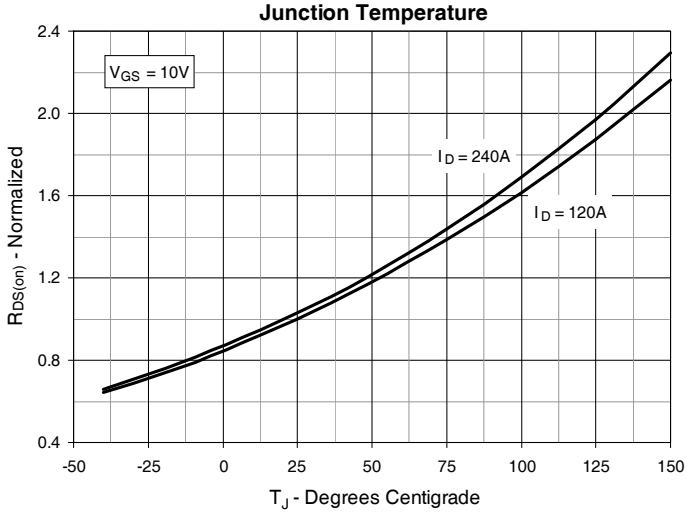


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 120A Value vs. Drain Current

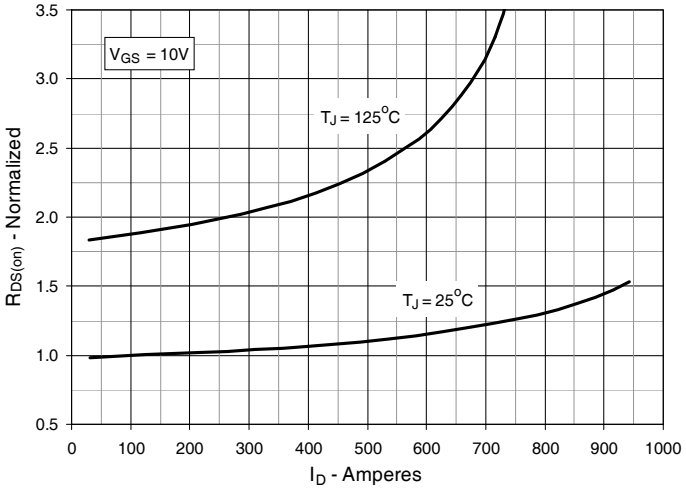


Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature

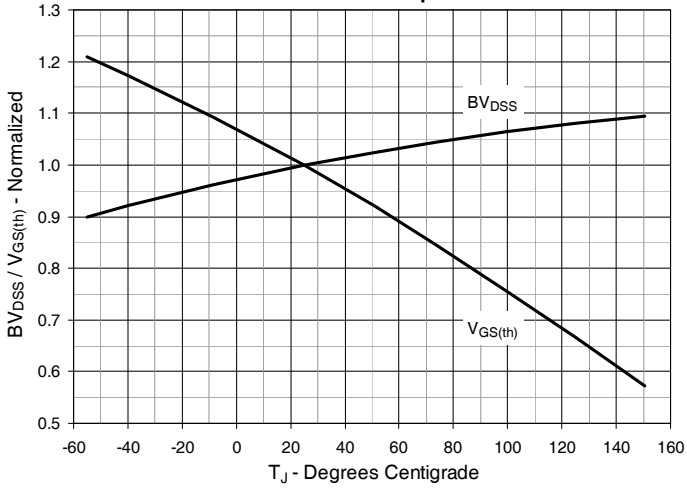


Fig. 7. Maximum Drain Current vs. Case Temperature

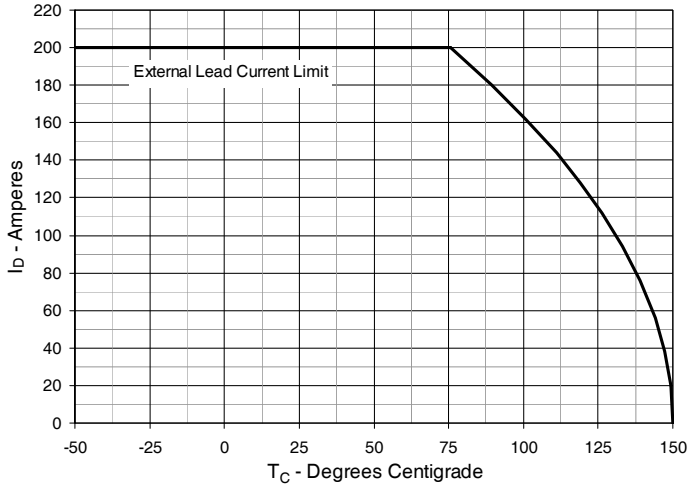


Fig. 8. Input Admittance

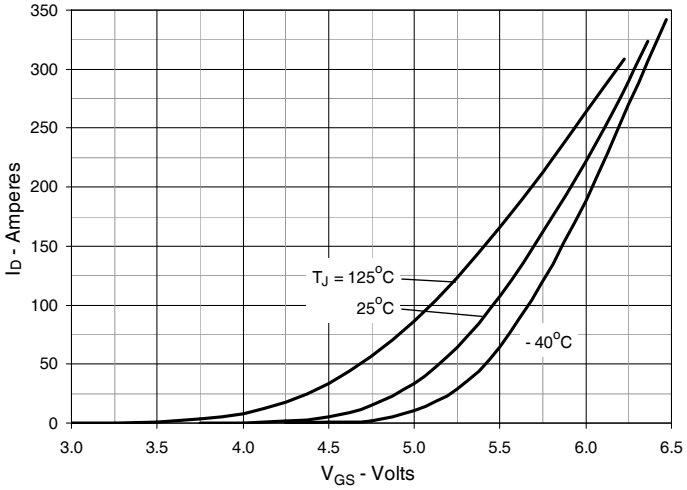


Fig. 9. Transconductance

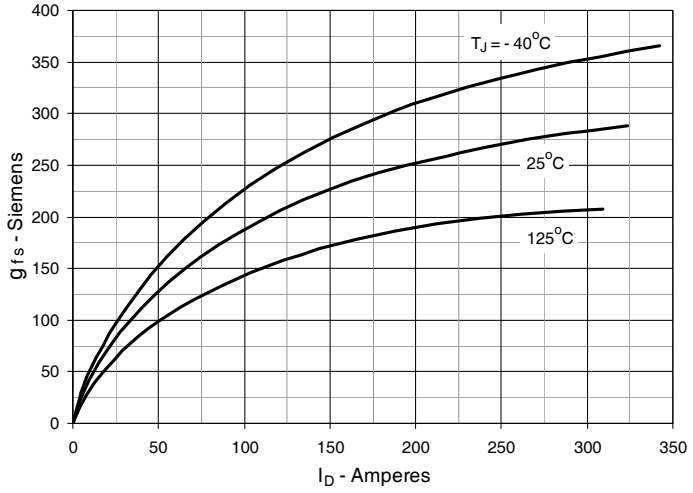


Fig. 10. Forward Voltage Drop of Intrinsic Diode

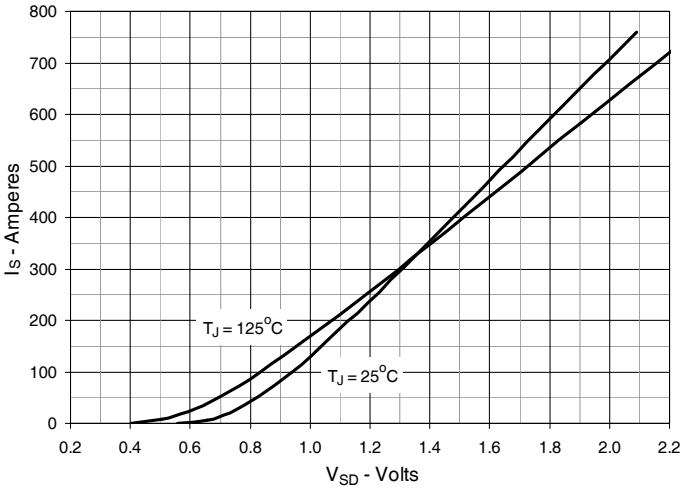


Fig. 11. Gate Charge

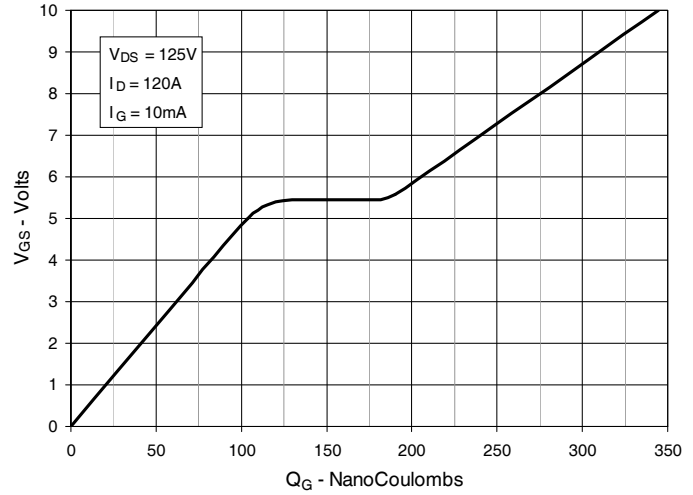


Fig. 12. Capacitance

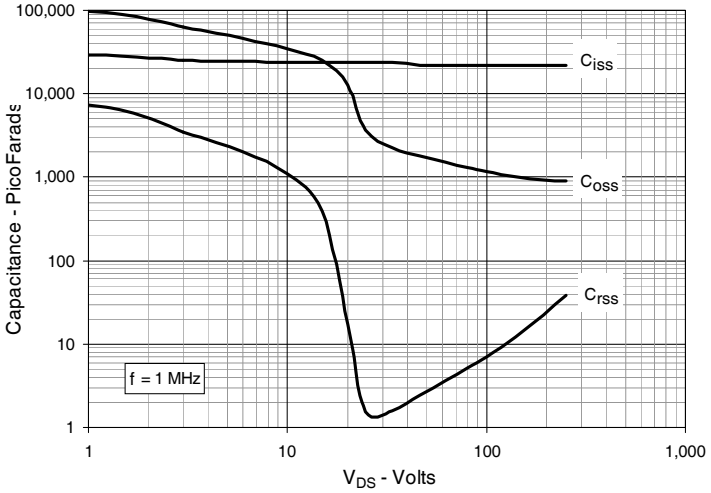


Fig. 7. Maximum Drain Current vs. Case Temperature

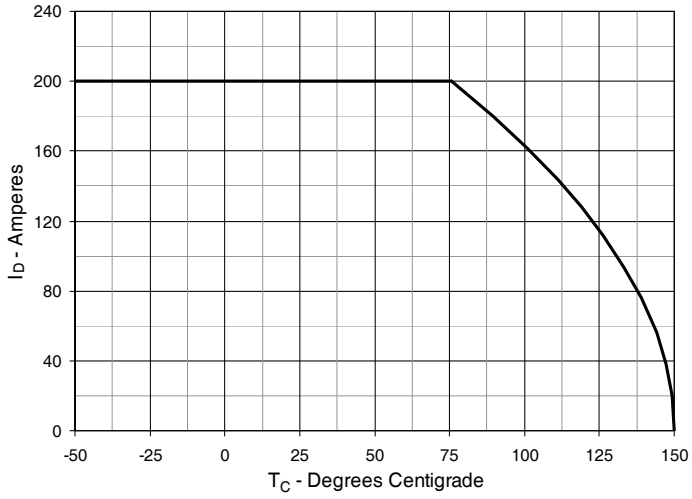


Fig. 14. Forward-Bias Safe Operating Area

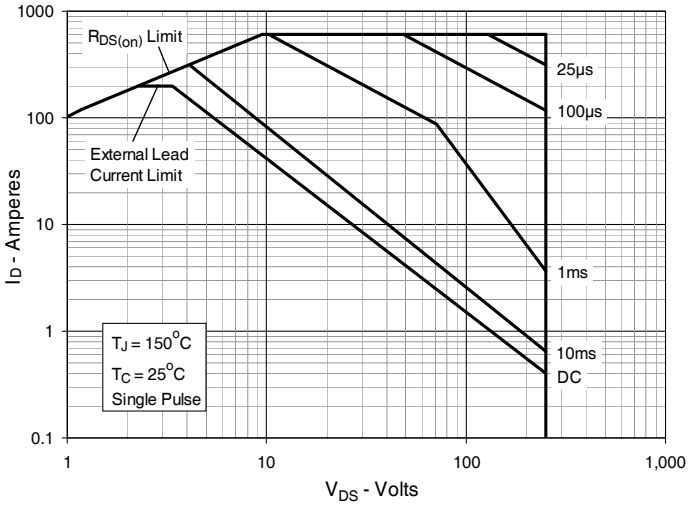


Fig. 15. Maximum Transient Thermal Impedance