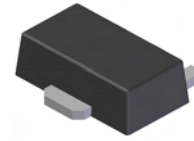
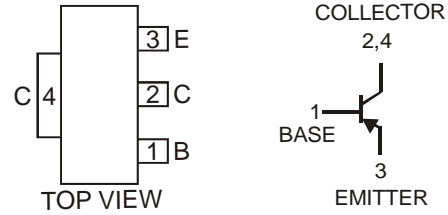


**Features**

- Epitaxial Planar Die Construction
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**



SOT89-3L



Schematic and Pin Configuration

**Mechanical Data**

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish — Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)

**Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CB0</sub>	-25	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-25	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Peak Pulse Current	I <sub>CM</sub>	-2	A
Continuous Collector Current	I <sub>C</sub>	-1	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	P <sub>D</sub>	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ T <sub>A</sub> = 25°C	R <sub>θJA</sub>	125	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Conditions
<b>OFF CHARACTERISTICS (Note 4)</b>						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-25	—	—	V	I <sub>C</sub> = -10μA, I <sub>E</sub> = 0
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	-25	—	—	V	I <sub>C</sub> = -1mA, I <sub>B</sub> = 0
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-5	—	—	V	I <sub>E</sub> = -10μA, I <sub>C</sub> = 0
Collector Cut-Off Current	I <sub>CBO</sub>	—	—	-0.1	μA	V <sub>CB</sub> = -20V, I <sub>E</sub> = 0
Emitter Cut-Off Current	I <sub>EBO</sub>	—	—	-0.1	μA	V <sub>EB</sub> = -4V, I <sub>C</sub> = 0
<b>ON CHARACTERISTICS (Note 4)</b>						
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	—	-0.15	-0.7	V	I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	—	-0.85	-1.2	V	I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA
DC Current Gain	h <sub>FE</sub>	140	—	280	—	V <sub>CE</sub> = -2V, I <sub>C</sub> = -50mA
		40	—	—	—	V <sub>CE</sub> = -2V, I <sub>C</sub> = -1A
<b>SMALL SIGNAL CHARACTERISTICS</b>						
Transition Frequency	f <sub>T</sub>	—	200	—	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -50mA f = 100MHz
Output Capacitance	C <sub>ob</sub>	—	12	—	pF	V <sub>CB</sub> = -10V, I <sub>E</sub> = 0, f = 1MHz

- Notes:
1. No purposefully added lead.
  2. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
  3. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
  4. Measured under pulsed conditions. Pulse width = 300μs. Duty cycle ≤2%.

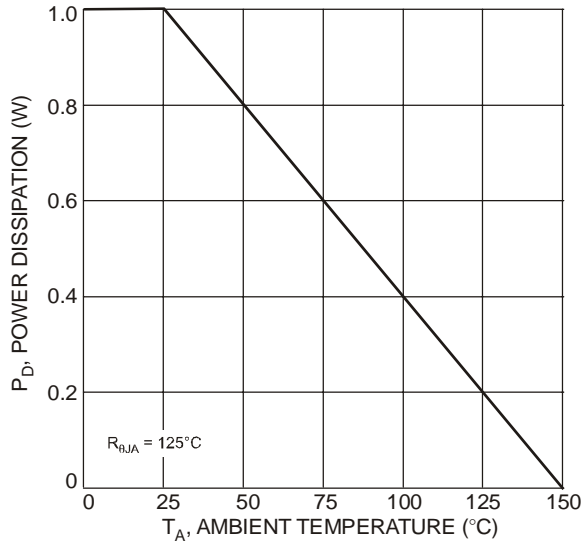


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

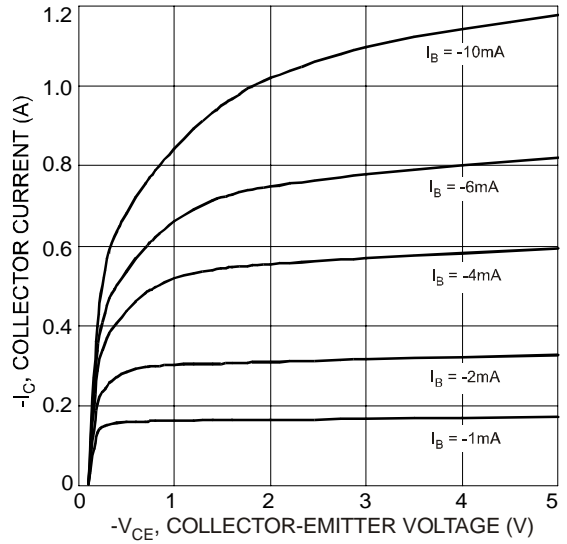


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

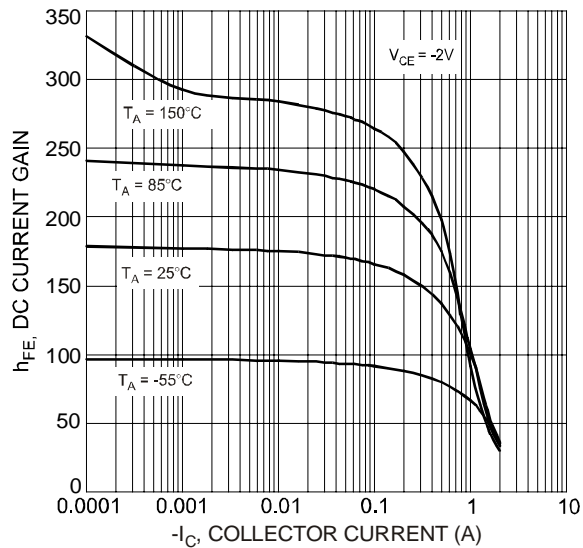


Fig. 3 Typical DC Current Gain vs. Collector Current

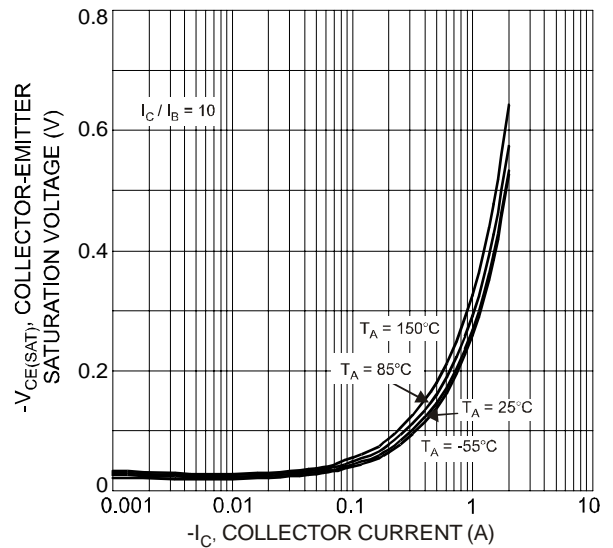


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

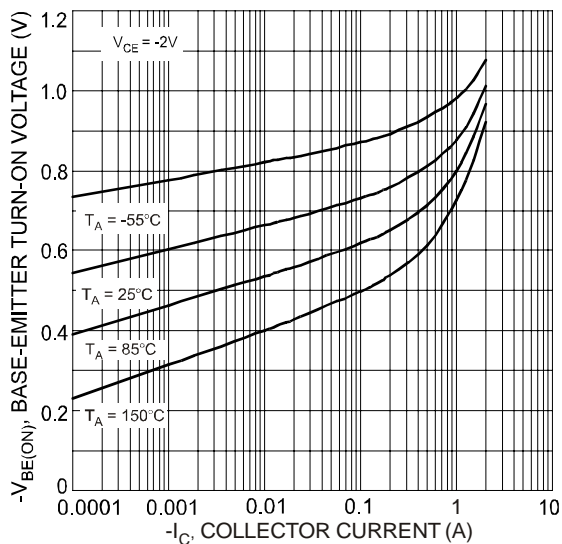


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

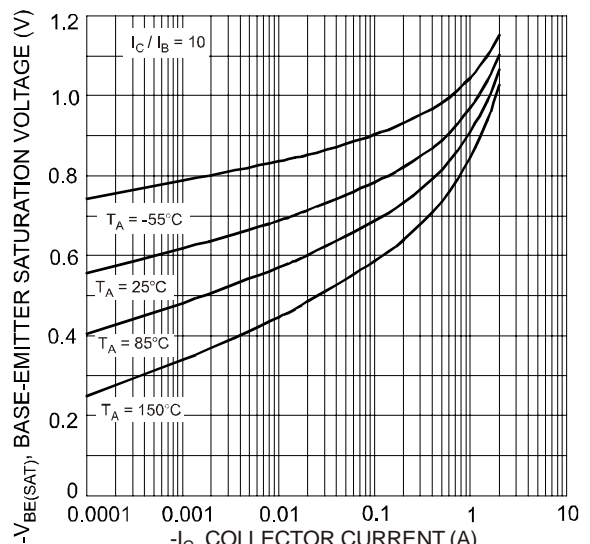


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

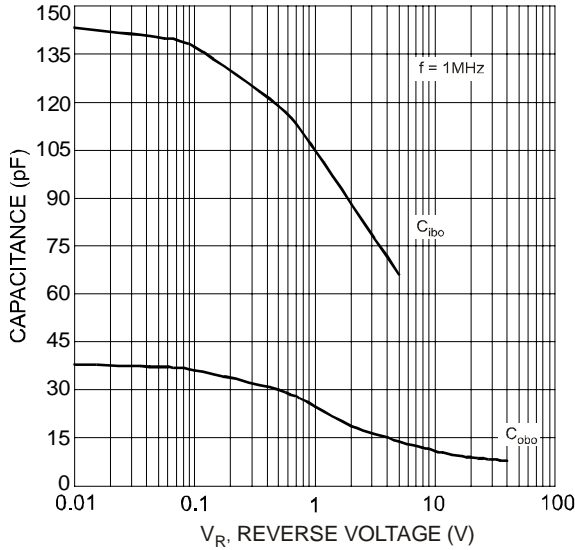


Fig. 7 Typical Capacitance Characteristics

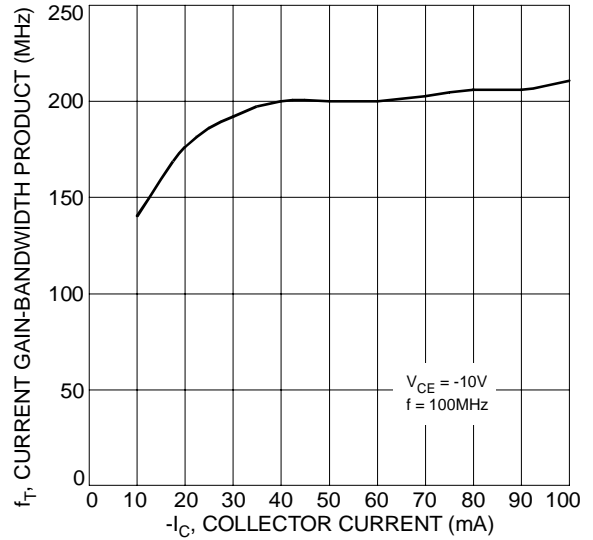


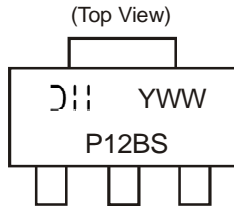
Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

**Ordering Information** (Note 5)

Device	Packaging	Shipping
2DB1119S-13	SOT89-3L	2500/Tape & Reel

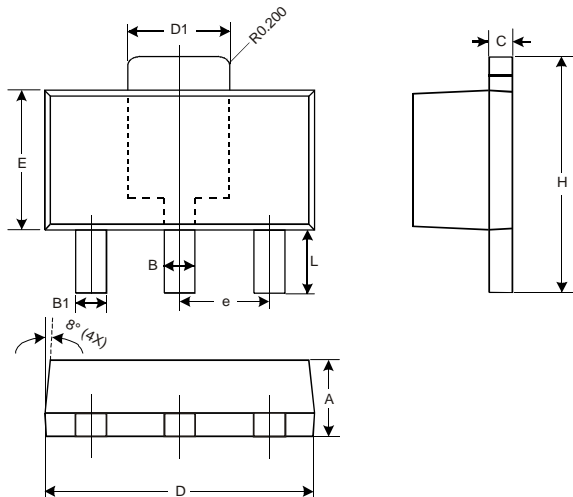
Notes: 5. For packaging details, please see below or go to our website at <http://www.diodes.com/ap02007.pdf>.

**Marking Information**



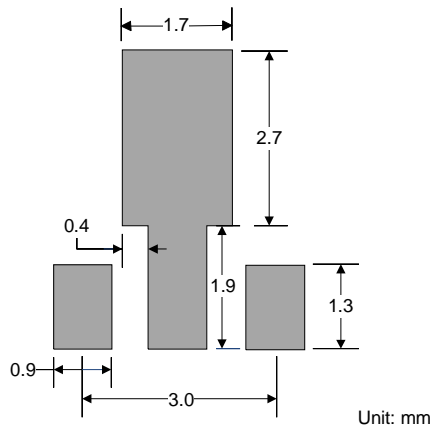
P12BS = Product Type Marking Code  
 YWW = Date Code Marking  
 Y = Last digit of year ex: 7 = 2007  
 WW = Week code 01 - 52

**Package Outline Dimensions**



SOT89-3L			
Dim	Min	Max	Typ
A	1.40	1.60	1.50
B	0.45	0.55	0.50
B1	0.37	0.47	0.42
C	0.35	0.43	0.38
D	4.40	4.60	4.50
D1	1.50	1.70	1.60
E	2.40	2.60	2.50
e	—	—	1.50
H	3.95	4.25	4.10
L	0.90	1.20	1.05
All Dimensions in mm			

## Suggested Pad Layout



### IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

### LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.