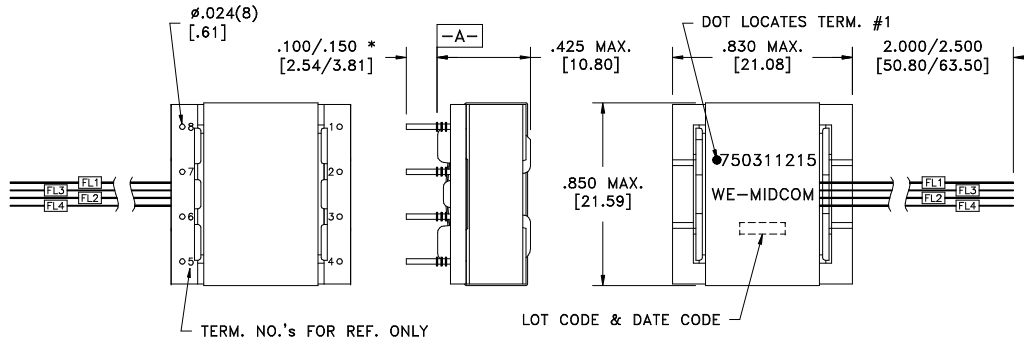


CUSTOMER TERMINAL	RoHS	LEAD(Pb)-FREE
Sn96%, Ag4%	Yes	Yes

more than you expect

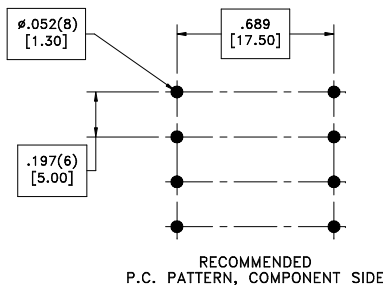
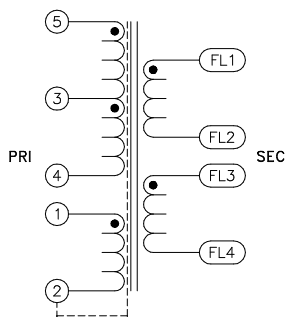


PART MUST INSERT FULLY TO SURFACE A IN RECOMMENDED GRID \* DIMENSION MAY BE EXCEEDED WITH SOLDER ONLY



TERM. NO.'s FOR REF. ONLY

LOT CODE & DATE CODE



RECOMMENDED P.C. PATTERN, COMPONENT SIDE

ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

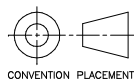
PARAMETER	TEST CONDITIONS	VALUE
D.C. RESISTANCE	5-4 @20°C	0.680 ohms ±10%
D.C. RESISTANCE	FL1-FL2 @20°C	0.057 ohms ±20%
D.C. RESISTANCE	FL3-FL4 @20°C	0.057 ohms ±20%
D.C. RESISTANCE	1-2 @20°C	0.203 ohms ±10%
INDUCTANCE	5-4 10kHz, 100mVAC, Ls	870uH ±10%
LEAKAGE INDUCTANCE	5-4 tie(1+2, FL1+FL2+FL3+FL4), 100kHz, 100mVAC, Ls	3.5uH typ., 6.0uH max.
DIELECTRIC	5-FL1 tie(FL2+FL3), 4.5kV, 1 minute	4.5kVAC, 1 second
URNS RATIO	(5-4):(FL1-FL2)	6.67:1, ±2%
URNS RATIO	(5-4):(FL3-FL4)	6.67:1, ±2%
URNS RATIO	(5-4):(1-2)	4:1, ±2%

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Designed to comply with the following requirements as defined by IEC61558-2-17:  
- Reinforced insulation for a primary circuit at a working voltage of 400VDC.

REV.	DATE	Method: Tray PKG-0058 www.midcom-inc.com
6A	3/10	SEE REVISION SHEET FOR REVISION LEVEL



CONVENTION PLACEMENT

Tolerances unless otherwise specified:  
Angles: ±1°      Decimals: ±.005 [.13]  
Fractions: ±1/64      Footprint: ±.001 [.03]

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE

**TRANSFORMER**

eiSos p/n: 750311215

PART NO.

**750311215**



SPECIFICATION SHEET 1 OF 1