


Table of Contents

2	NOTES
3	MAIN SCHEMATIC

Revisions

Rev	Description	Date	Approved
A	ORIGINAL RELEASE	11/04/10	J.CAZARES
B	RE-SPIN PER ECO29864	02/08/11	J.CAZARES

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<small>Designer:</small> A. ARIAS		<small>Drawing Title:</small> XUSB2SER	
<small>Drawn by:</small> A. ARIAS		<small>Page Title:</small> TITLE PAGE	
<small>Approved:</small> J. CAZAREZ	<small>Size:</small> C	<small>Document Number:</small> SCH-26933 PDF: SPF-26933	<small>Rev:</small> B
<small>Date:</small> Thursday, February 17, 2011		<small>Sheet:</small> 1 of 3	

1. Unless Otherwise Specified:

All resistors are in ohms, 5%, 1/8 Watt

All capacitors are in uF, 20%, 50V

All voltages are DC

All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:

_B Denotes - Active-Low Signal

<> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification: FCP: _____ FIUC: X PUB: _____

Drawing Title: **XUSB2SER**

Page Title: **NOTES**

Size C	Document Number SCH-26933 PDF: SPF-26933	Rev B
Date: Thursday, February 17, 2011	Sheet 2	of 3

