

NDF10N62Z

N-Channel Power MOSFET 620 V, 0.75 Ω

Features

- Low ON Resistance
- Low Gate Charge
- ESD Diode-Protected Gate
- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

| Rating | Symbol | NDF10N62Z | Unit |
|---|----------------|--------------|------------------|
| Drain-to-Source Voltage | V_{DSS} | 620 | V |
| Continuous Drain Current, $R_{\theta JC}$ (Note 2) | I_D | 10 | A |
| Continuous Drain Current $R_{\theta JC}$, $T_A = 100^\circ\text{C}$ (Note 2) | I_D | 5.7 | A |
| Pulsed Drain Current, $V_{GS} @ 10\text{ V}$ | I_{DM} | 36 | A |
| Power Dissipation, $R_{\theta JC}$ (Note 1) | P_D | 36 | W |
| Gate-to-Source Voltage | V_{GS} | ± 30 | V |
| Single Pulse Avalanche Energy, $I_D = 10\text{ A}$ | E_{AS} | 300 | mJ |
| ESD (HBM) (JESD22-A114) | V_{ESD} | 3900 | V |
| RMS Isolation Voltage ($t = 0.3\text{ sec.}$, R.H. $\leq 30\%$, $T_A = 25^\circ\text{C}$) (Figure 14) | V_{ISO} | 4500 | V |
| Peak Diode Recovery | dv/dt | 4.5 (Note 3) | V/ns |
| Continuous Source Current (Body Diode) | I_S | 10 | A |
| Maximum Temperature for Soldering Leads | T_L | 260 | $^\circ\text{C}$ |
| Operating Junction and Storage Temperature Range | T_J, T_{stg} | -55 to 150 | $^\circ\text{C}$ |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

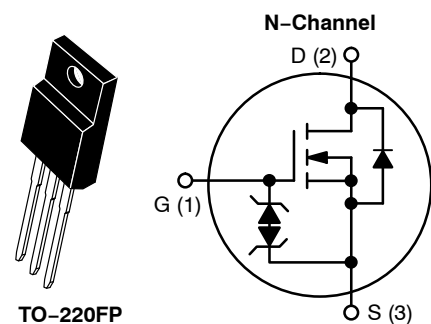
1. Surface mounted on FR4 board using 1" sq. pad size, (Cu area = 1.127 in sq [2 oz] including traces)
2. Limited by maximum junction temperature
3. $I_S \leq 10\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$, $V_{DD} = 80\% BV_{DSS}$



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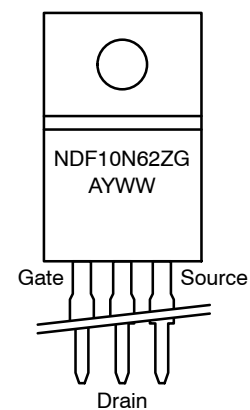
<http://onsemi.com>

| V_{DSS} | $R_{DS(ON)} (MAX) @ 5\text{ A}$ |
|-----------|---------------------------------|
| 620 V | 0.75 Ω |



TO-220FP
CASE 221D
STYLE 1

MARKING DIAGRAM



- A = Location Code
- Y = Year
- WW = Work Week
- G = Pb-Free, Halogen-Free Package

ORDERING INFORMATION

| Device | Package | Shipping |
|------------|--|---------------|
| NDF10N62ZG | TO-220FP (Pb-Free, Halogen-Free) | 50 Units/Rail |

NDF10N62Z

THERMAL RESISTANCE

| Parameter | Symbol | NDF10N62Z | Unit |
|---|-----------------|-----------|------|
| Junction-to-Case (Drain) | $R_{\theta JC}$ | 3.4 | °C/W |
| Junction-to-Ambient Steady State (Note 4) | $R_{\theta JA}$ | 50 | |

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic | Test Conditions | Symbol | Min | Typ | Max | Unit |
|----------------|-----------------|--------|-----|-----|-----|------|
|----------------|-----------------|--------|-----|-----|-----|------|

OFF CHARACTERISTICS

| | | | | | | |
|---|--|------------------------------|-------|-----|-----|------|
| Drain-to-Source Breakdown Voltage | $V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$ | BV_{DSS} | 620 | | | V |
| Breakdown Voltage Temperature Coefficient | Reference to 25°C , $I_D = 1\text{ mA}$ | $\Delta BV_{DSS}/\Delta T_J$ | | 0.6 | | V/°C |
| Drain-to-Source Leakage Current | $V_{DS} = 620\text{ V}, V_{GS} = 0\text{ V}$ | I_{DSS} | 25°C | | 1 | μA |
| | | | 125°C | | 50 | |
| Gate-to-Source Forward Leakage | $V_{GS} = \pm 20\text{ V}$ | I_{GSS} | | | ±10 | μA |

ON CHARACTERISTICS (Note 5)

| | | | | | | |
|--------------------------------------|---|--------------|-----|------|------|---|
| Static Drain-to-Source On-Resistance | $V_{GS} = 10\text{ V}, I_D = 5.0\text{ A}$ | $R_{DS(on)}$ | | 0.65 | 0.75 | Ω |
| Gate Threshold Voltage | $V_{DS} = V_{GS}, I_D = 100\text{ }\mu\text{A}$ | $V_{GS(th)}$ | 3.0 | | 4.5 | V |
| Forward Transconductance | $V_{DS} = 15\text{ V}, I_D = 10\text{ A}$ | g_{FS} | | 7.9 | | S |

DYNAMIC CHARACTERISTICS

| | | | | | | |
|---------------------------------|---|-----------|--|------|--|----|
| Input Capacitance | $V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$ | C_{iss} | | 1425 | | pF |
| Output Capacitance | | C_{oss} | | 150 | | |
| Reverse Transfer Capacitance | | C_{rss} | | 35 | | |
| Total Gate Charge | $V_{DD} = 310\text{ V}, I_D = 10\text{ A},$ $V_{GS} = 10\text{ V}$ | Q_g | | 47 | | nC |
| Gate-to-Source Charge | | Q_{gs} | | 9.3 | | |
| Gate-to-Drain ("Miller") Charge | | Q_{gd} | | 25 | | |
| Plateau Voltage | | V_{gp} | | 6.4 | | V |
| Gate Resistance | | R_g | | 1.5 | | Ω |

RESISTIVE SWITCHING CHARACTERISTICS

| | | | | | | |
|---------------------|--|--------------|--|----|--|----|
| Turn-On Delay Time | $V_{DD} = 310\text{ V}, I_D = 10\text{ A},$ $V_{GS} = 10\text{ V}, R_G = 5\text{ }\Omega$ | $t_{d(on)}$ | | 15 | | ns |
| Rise Time | | t_r | | 31 | | |
| Turn-Off Delay Time | | $t_{d(off)}$ | | 40 | | |
| Fall Time | | t_f | | 21 | | |

SOURCE-DRAIN DIODE CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

| | | | | | | |
|-------------------------|--|----------|--|-----|-----|----|
| Diode Forward Voltage | $I_S = 10\text{ A}, V_{GS} = 0\text{ V}$ | V_{SD} | | | 1.6 | V |
| Reverse Recovery Time | $V_{GS} = 0\text{ V}, V_{DD} = 30\text{ V}$ $I_S = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$ | t_{rr} | | 395 | | ns |
| Reverse Recovery Charge | | Q_{rr} | | 3.0 | | μC |

4. Insertion mounted

5. Pulse Width $\leq 380\text{ }\mu\text{s}$, Duty Cycle $\leq 2\%$.

NDF10N62Z

TYPICAL CHARACTERISTICS

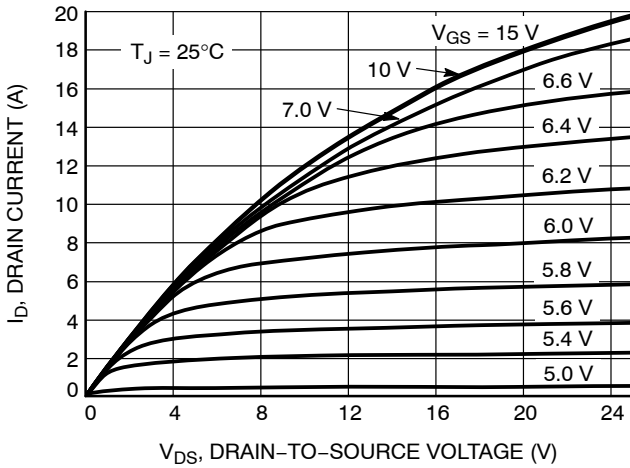


Figure 1. On-Region Characteristics

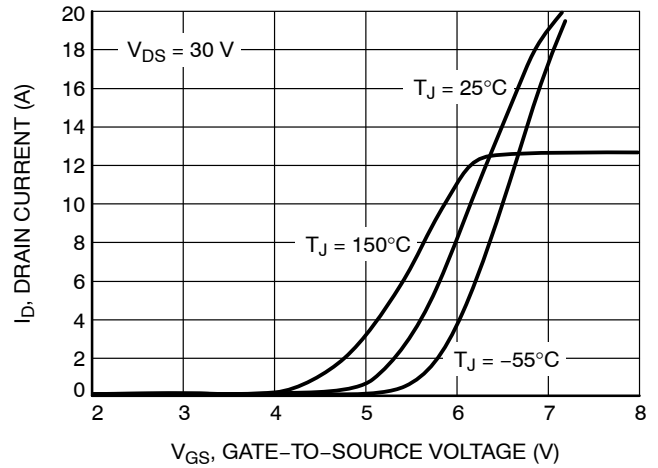


Figure 2. Transfer Characteristics

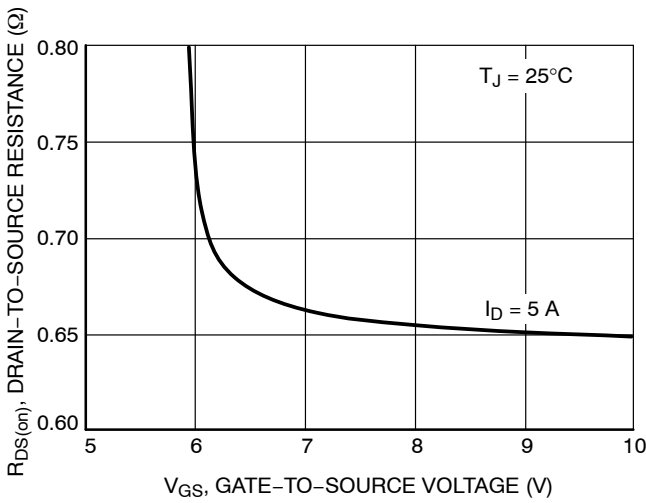


Figure 3. On-Resistance vs. Gate Voltage

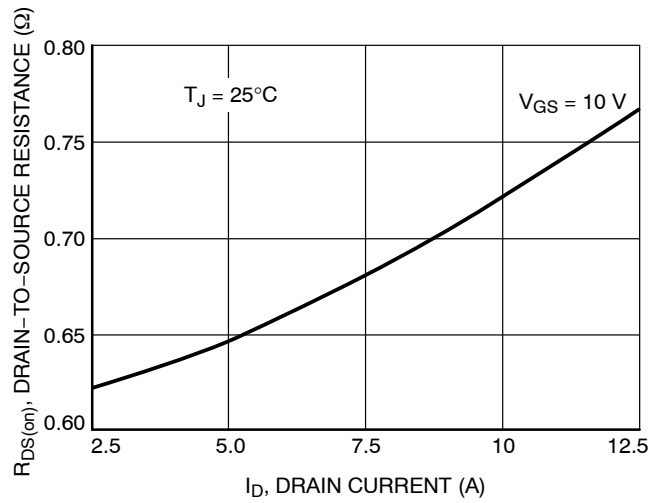


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

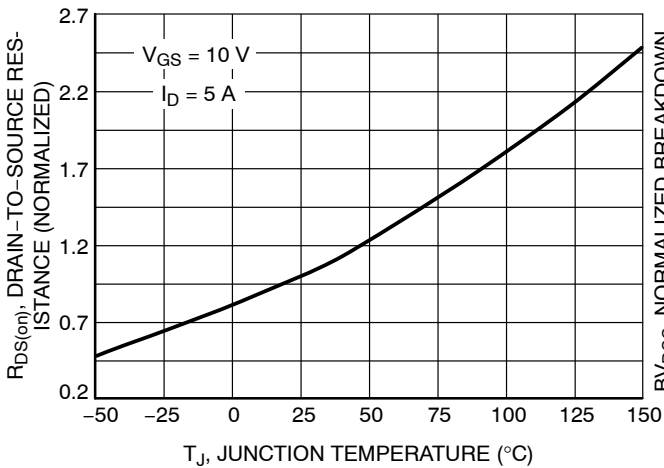


Figure 5. On-Resistance Variation with Temperature

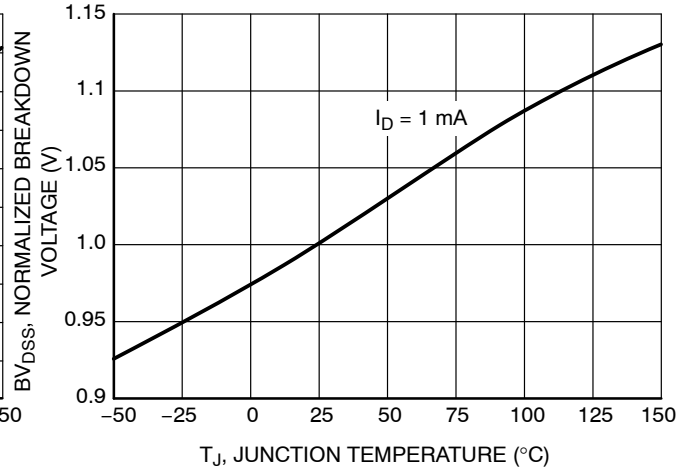


Figure 6. BV_{DSS} Variation with Temperature

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TYPICAL CHARACTERISTICS

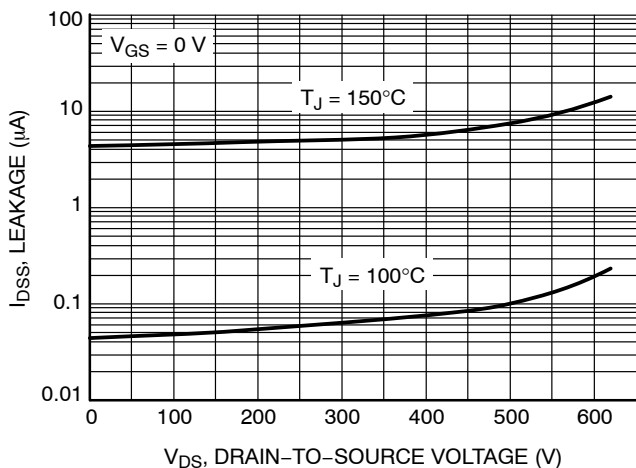


Figure 7. Drain-to-Source Leakage Current vs. Voltage

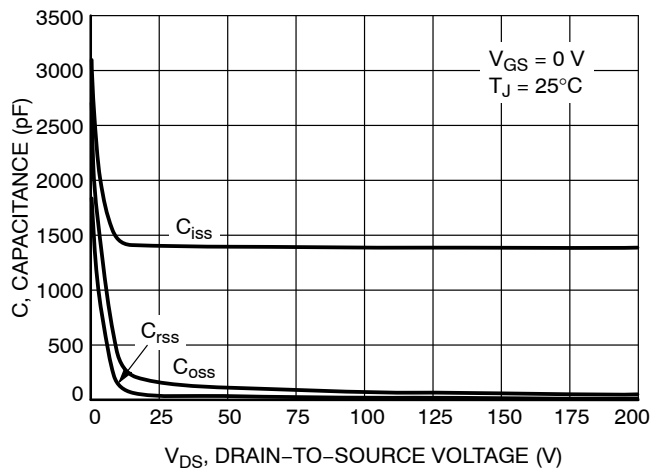


Figure 8. Capacitance Variation

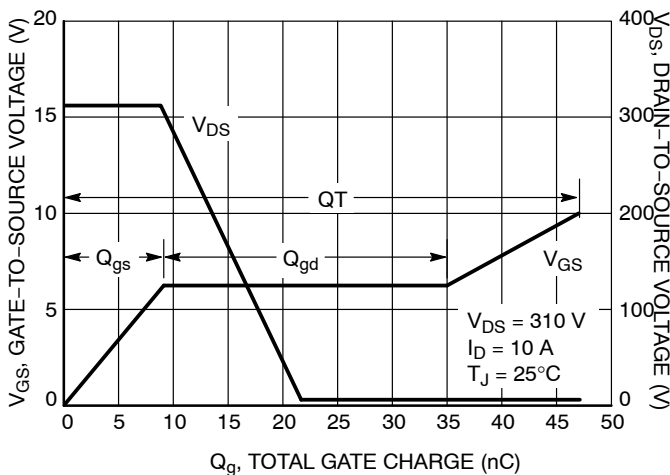


Figure 9. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

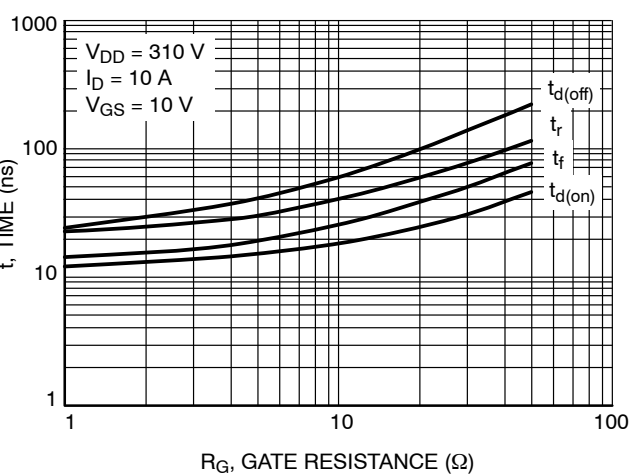


Figure 10. Resistive Switching Time Variation vs. Gate Resistance

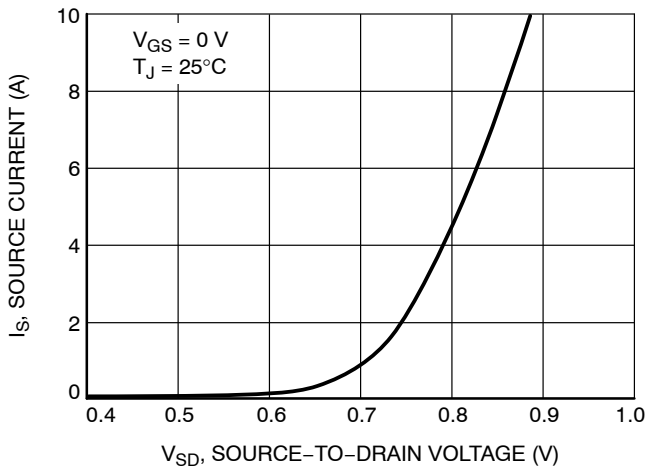


Figure 11. Diode Source Current vs. Forward Voltage

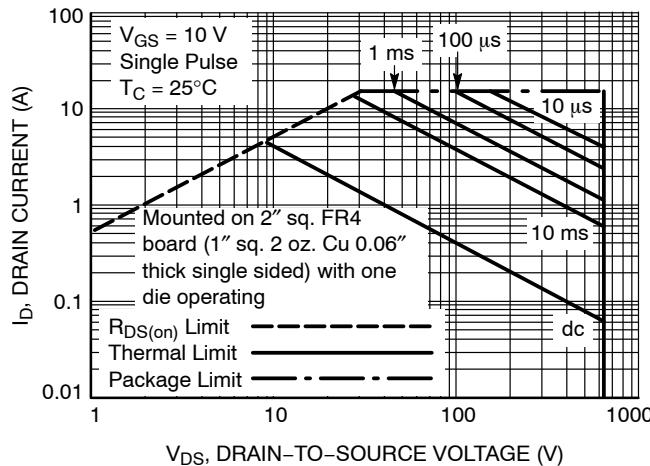


Figure 12. Maximum Rated Forward Biased Safe Operating Area for NDF10N62Z

NDF10N62Z

TYPICAL CHARACTERISTICS

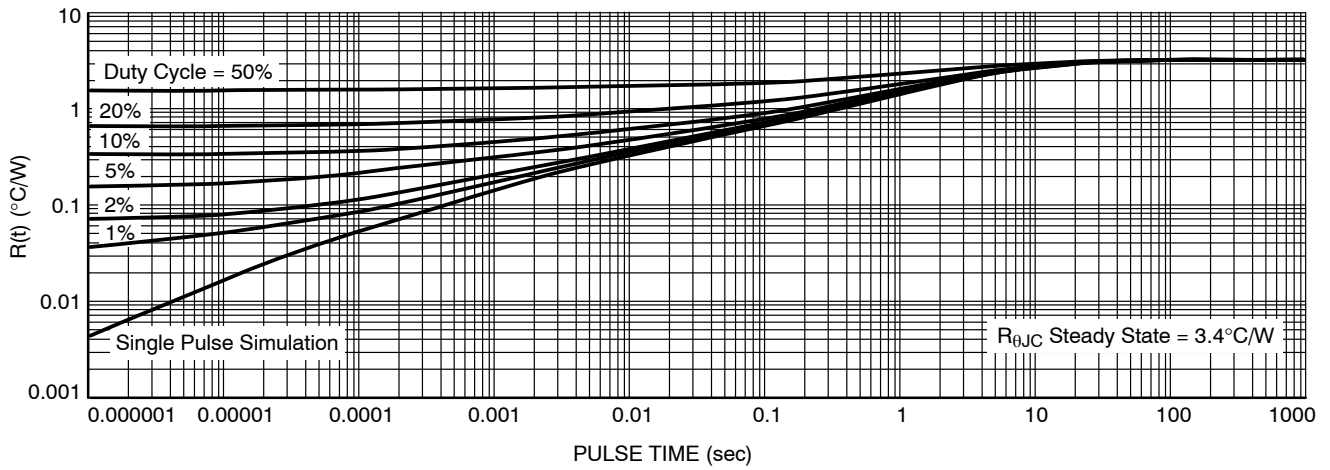


Figure 13. Thermal Impedance for NDF10N62Z

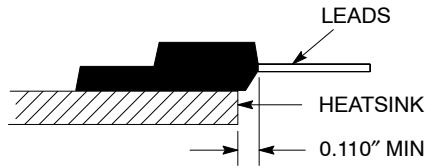


Figure 14. Isolation Test Diagram

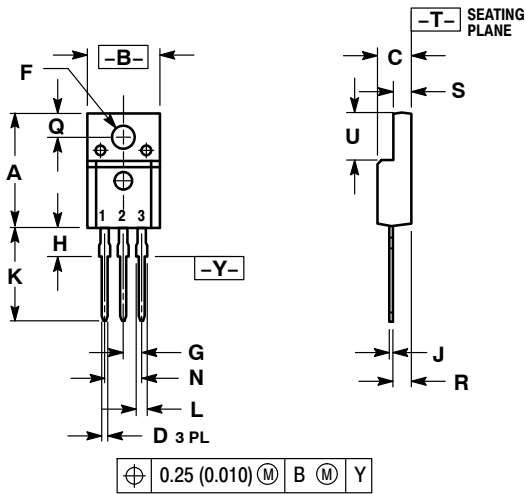
Measurement made between leads and heatsink with all leads shorted together.

*For additional mounting information, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NDF10N62Z

PACKAGE DIMENSIONS

TO-220FP
CASE 221D-03
ISSUE K



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH
3. 221D-01 THRU 221D-02 OBSOLETE, NEW STANDARD 221D-03.

| DIM | INCHES | | MILLIMETERS | |
|-----|-----------|-------|-------------|-------|
| | MIN | MAX | MIN | MAX |
| A | 0.617 | 0.635 | 15.67 | 16.12 |
| B | 0.392 | 0.419 | 9.96 | 10.63 |
| C | 0.177 | 0.193 | 4.50 | 4.90 |
| D | 0.024 | 0.039 | 0.60 | 1.00 |
| F | 0.116 | 0.129 | 2.95 | 3.28 |
| G | 0.100 BSC | | 2.54 BSC | |
| H | 0.118 | 0.135 | 3.00 | 3.43 |
| J | 0.018 | 0.025 | 0.45 | 0.63 |
| K | 0.503 | 0.541 | 12.78 | 13.73 |
| L | 0.048 | 0.058 | 1.23 | 1.47 |
| N | 0.200 BSC | | 5.08 BSC | |
| Q | 0.122 | 0.138 | 3.10 | 3.50 |
| R | 0.099 | 0.117 | 2.51 | 2.96 |
| S | 0.092 | 0.113 | 2.34 | 2.87 |
| U | 0.239 | 0.271 | 6.06 | 6.88 |

STYLE 1:

- PIN 1. GATE
2. DRAIN
3. SOURCE

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